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**UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Martin Ceredig Roberts et al.

Title: METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL POLY PROCESS

Docket No.: 303.451US6

Serial No.: 09/745,780

Filed: December 21, 2000

Due Date: March 6, 2002

Examiner: Neal Berezny

Group Art Unit: 2823

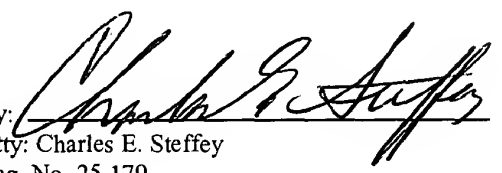
Commissioner for Patents  
Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Amendment and Response (6 Pages).
- ☒ 1 sheet of red-lined drawings.
- ☒ A Clean Version of Pending Claims (11 pgs.).
- ☒ A copy of Form 1449 as filed December 21, 2000 (1 pg.) along with courtesy copies of 12 references.
- ☒ A Supplemental Information Disclosure Statement (1 pg.), Form 1449 (1 pg.), and copies of 2 cited references.


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